

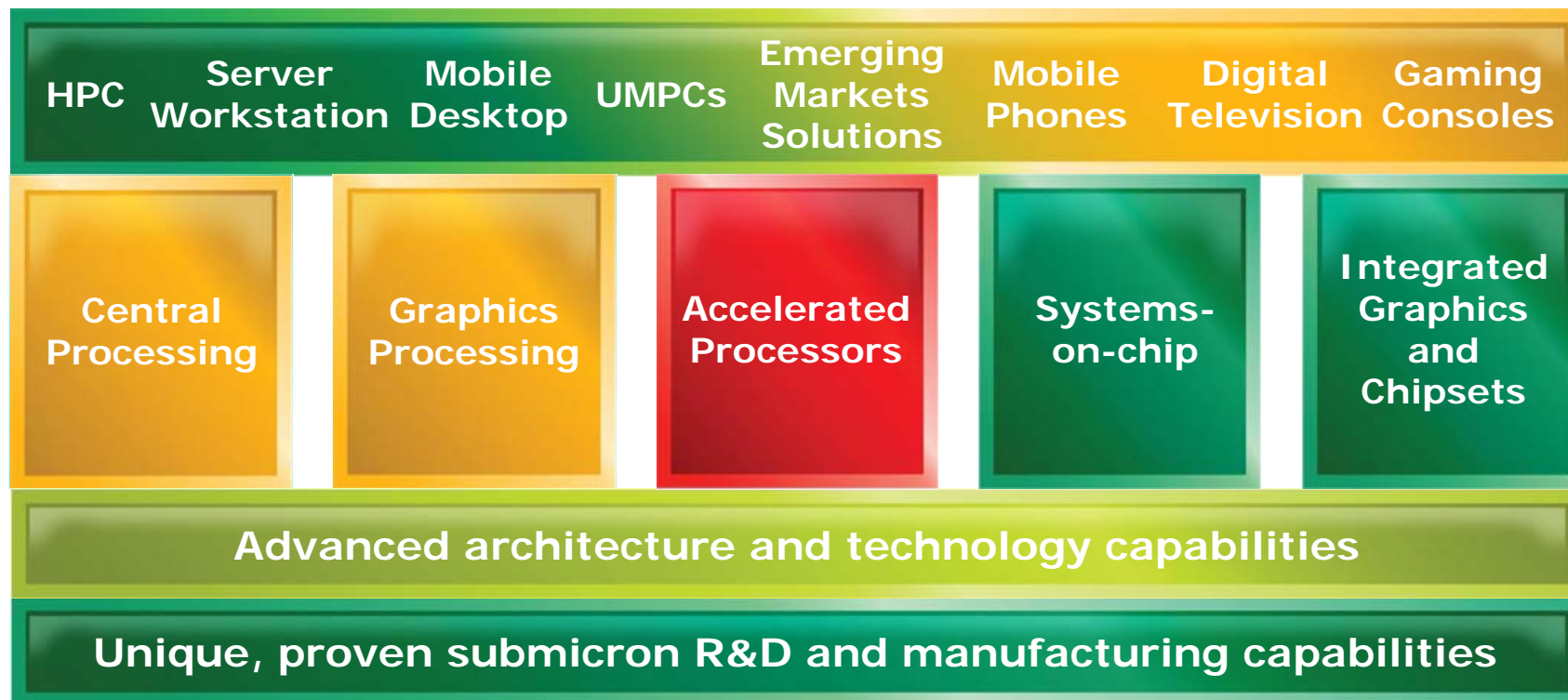
Phil Hester
Senior VP and Chief Technology Officer

December 14, 2006

Unleashing the Processing Powerhouse



Bridging traditional computing and consumer electronics



An aligned vision for the PC and CE markets

3-Year MPU Development Priorities

Next-Gen Oct-Core Server Processor

The choice for the most demanding, multi-threaded software

Stream Computing Rollout

Bringing the immense GPU compute capabilities to HPC

Advanced Software Functionality

Ecosystem-based, collaborative approach for superior results

Torrenza Expansion

Optimization and differentiation through open innovation

First "Fusion" Client Processors

Step-function improvement performance-per-watt-per-dollar

Platform Graphics IP Integration

Superior Microsoft Windows Vista™ experience

Process Technology Evolution

Sustained Technology Innovation



65nm

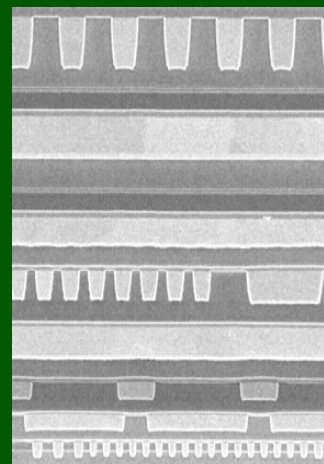
Key innovations:

- Embedded Silicon Germanium and stress memory technology
- Next-generation stress engineering
- Shipping for revenue now

45nm

Key innovations:

- Immersion lithography
- Ultra low-K dielectric interconnect



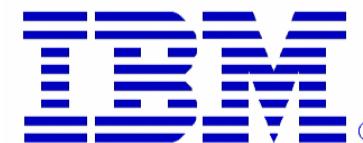
4x: Low-k
 $k = 3.0$

2x: Ultra
Low-k
 $k = 2.4$

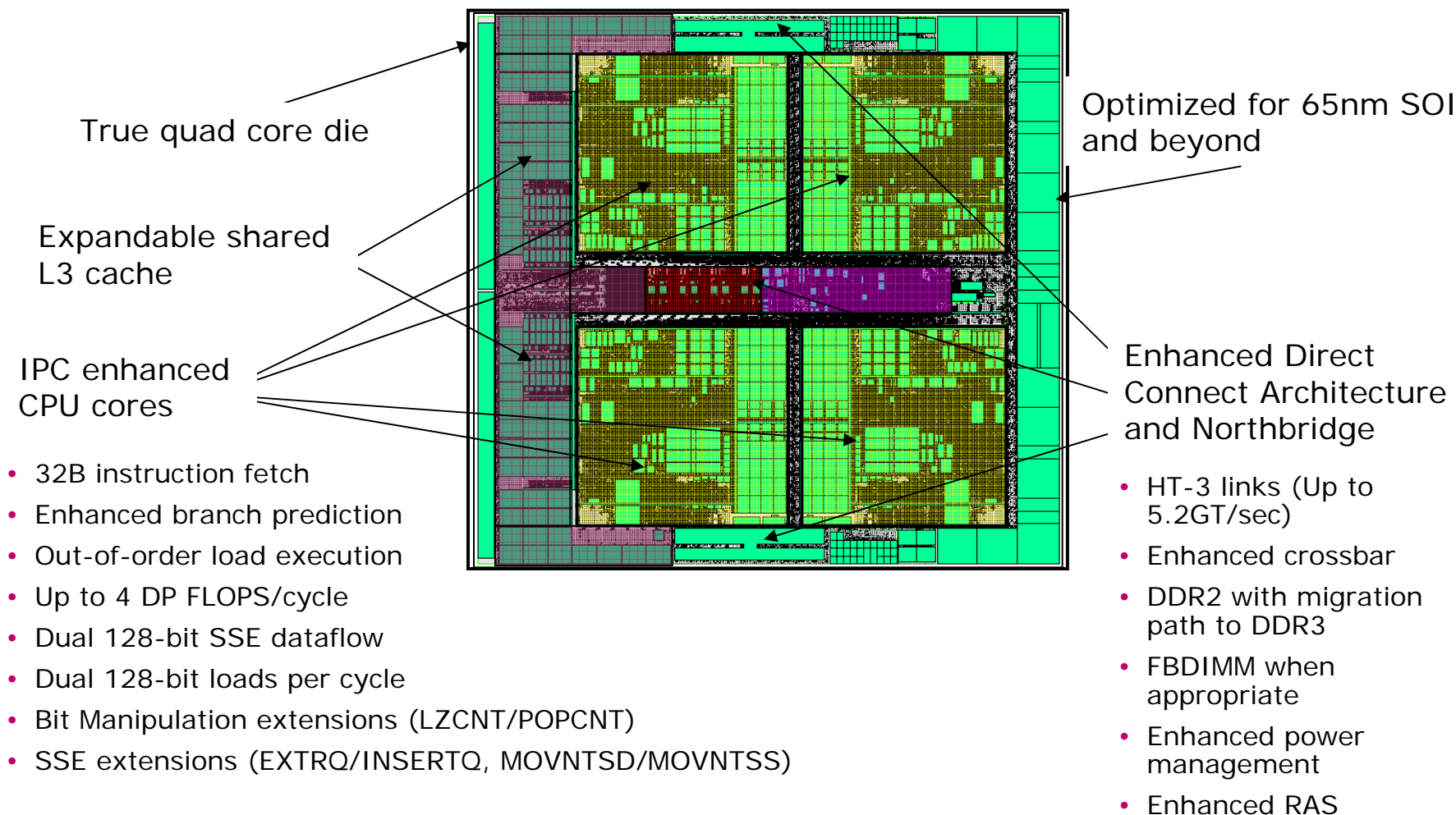
1x: Low-k
 $k = 2.7$

Will enter production in mid-2008

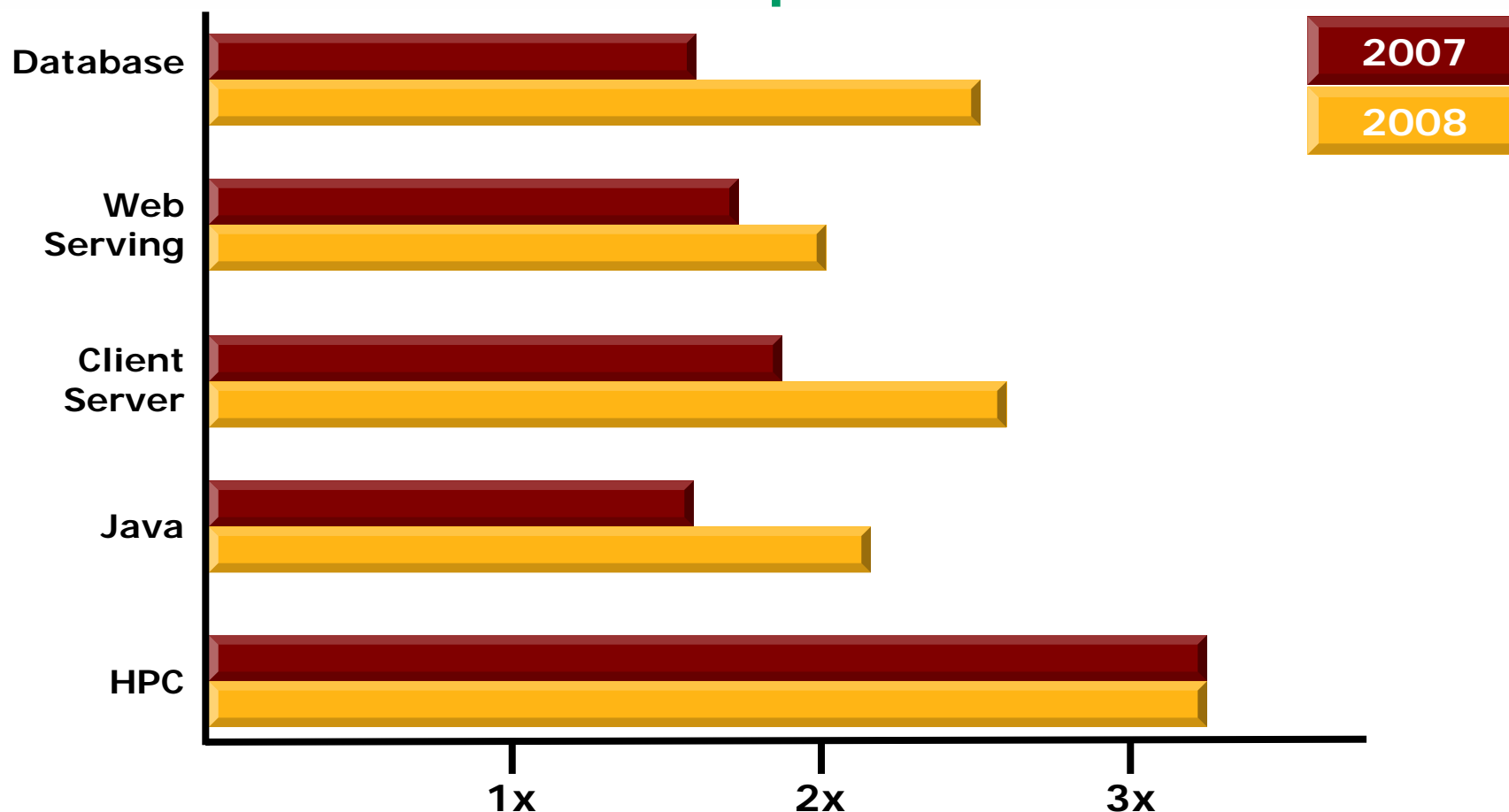
AMD & IBM's research and development collaboration continues to drive process technology innovation



A Closer Look at AMD's Next Generation Server and Desktop Architecture: Barcelona



Critical Performance Increases Where it Matters Most for Enterprises

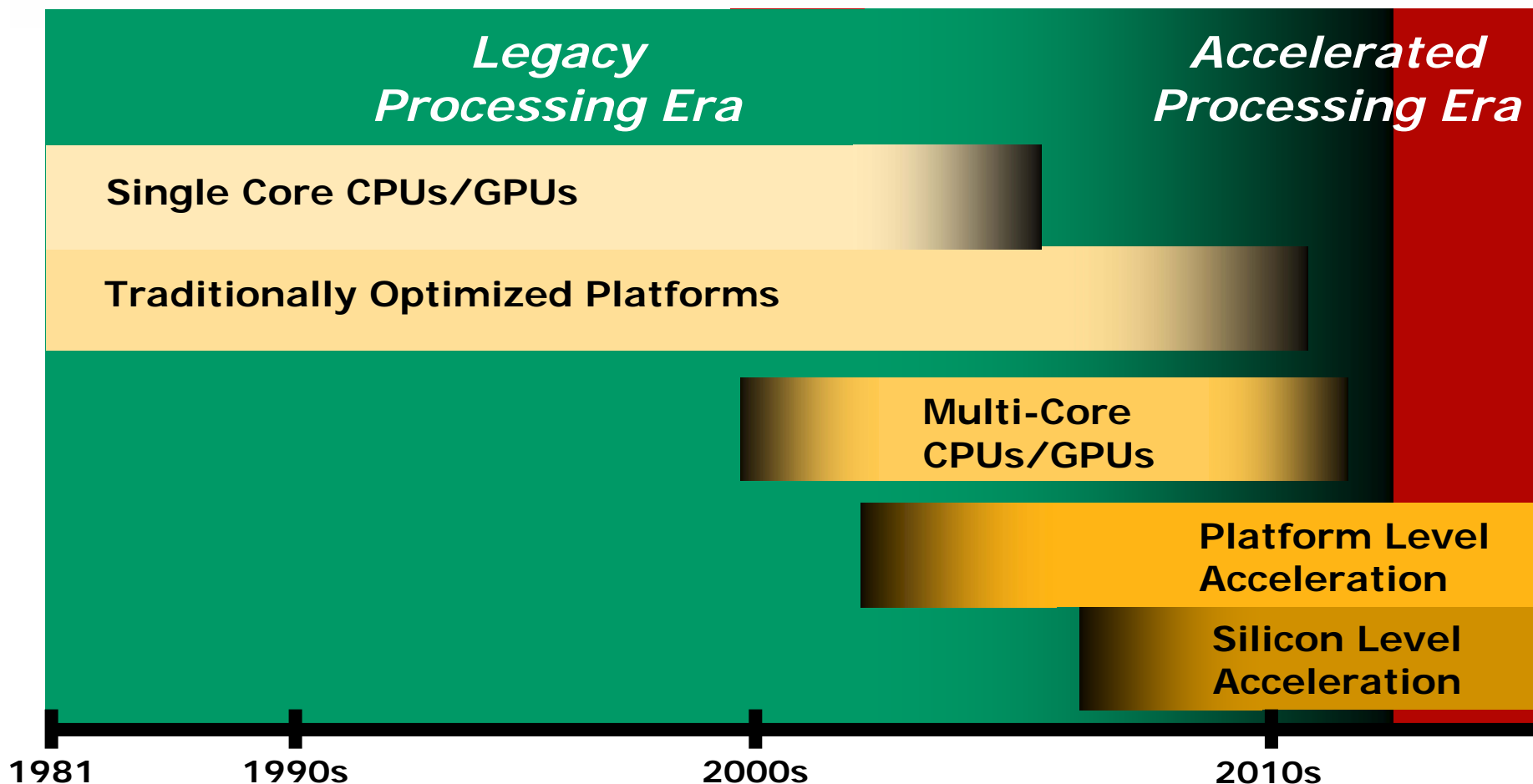


Application Performance Improvement

Performance projections based on modeling and a baseline of 2006 performance

Source: AMD planned

The Next Major x86 Inflection Point

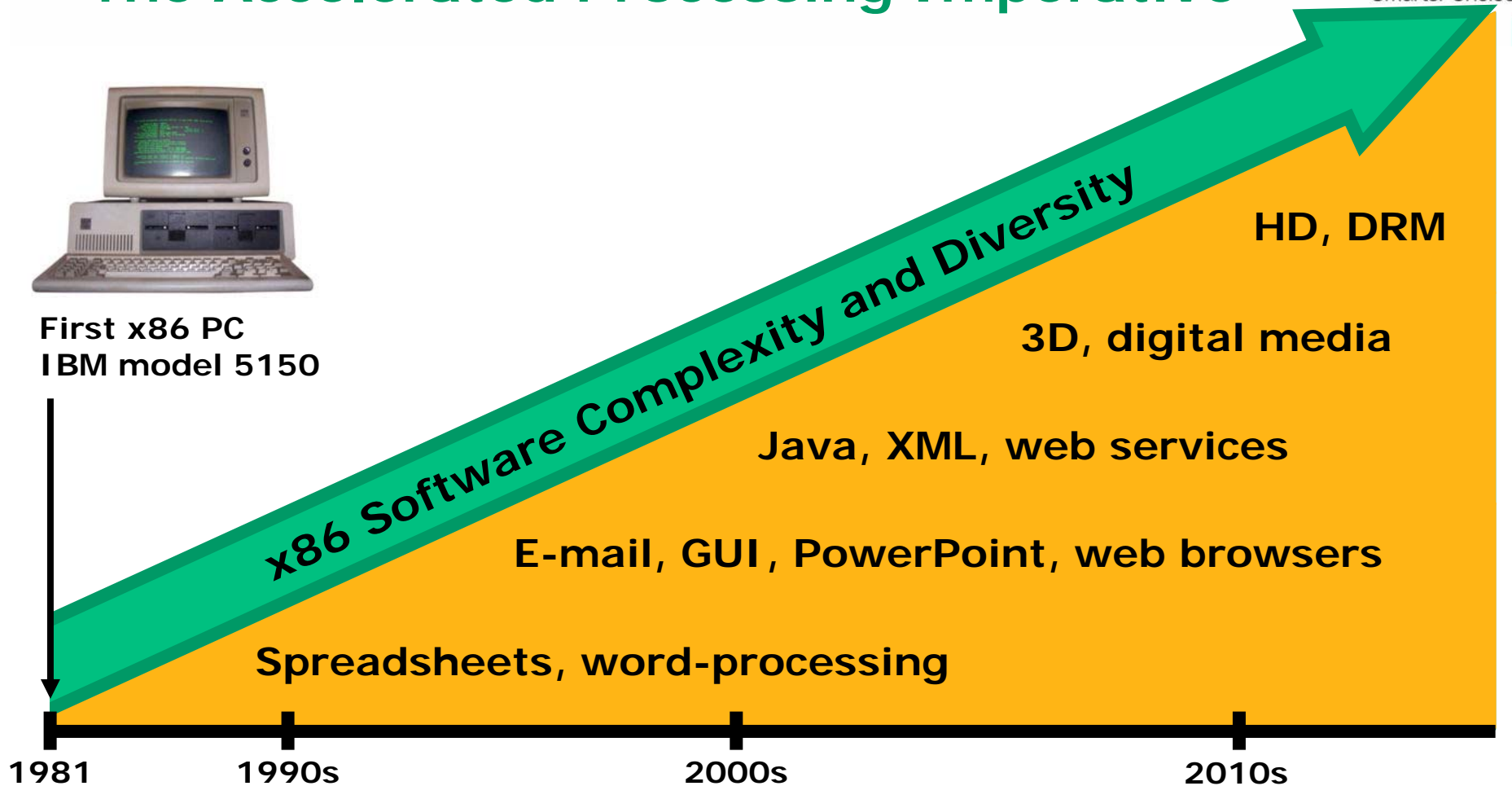


**The Era of Accelerated Computing is coming,
and AMD is again leading the way**

The Accelerated Processing Imperative

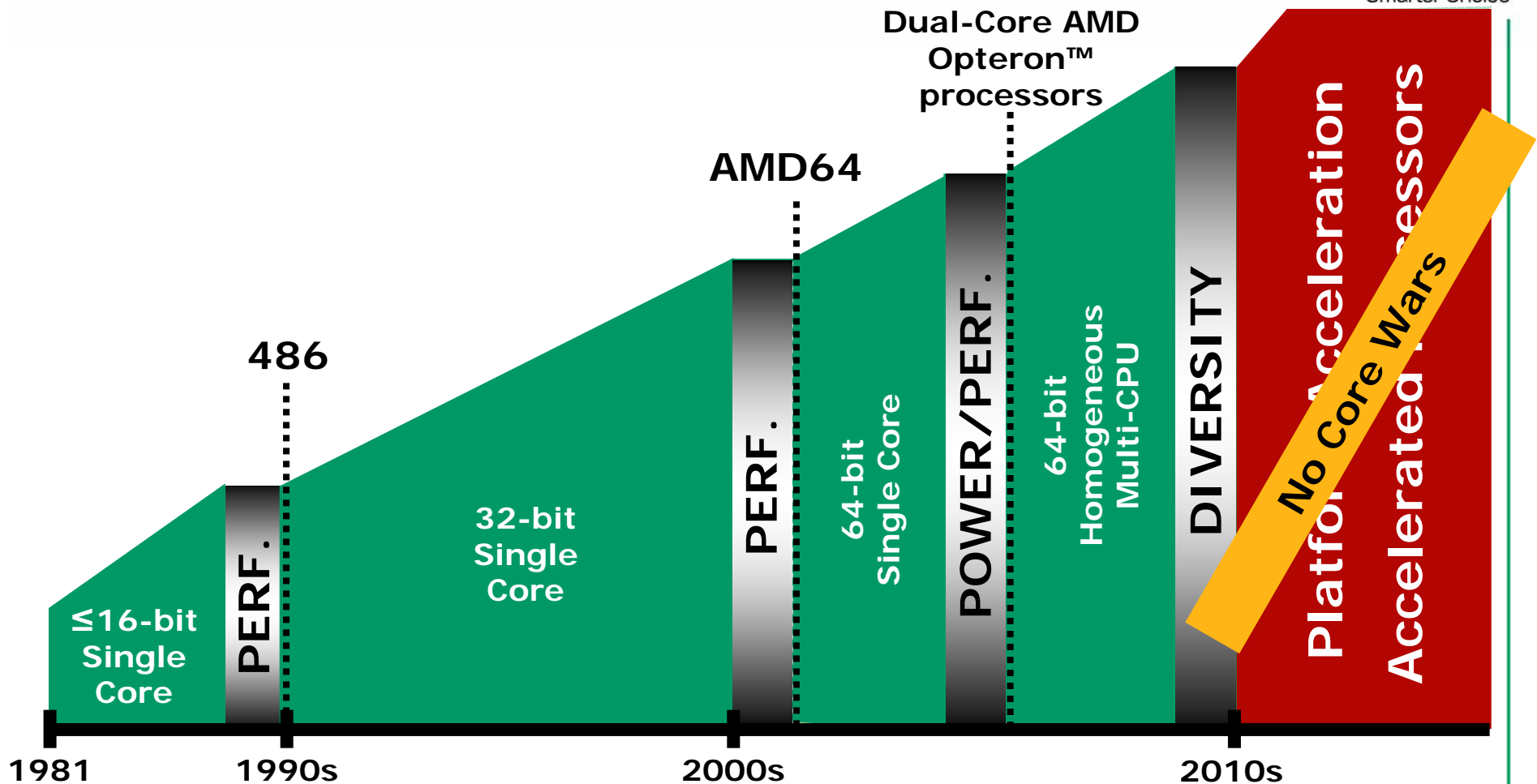


First x86 PC
IBM model 5150



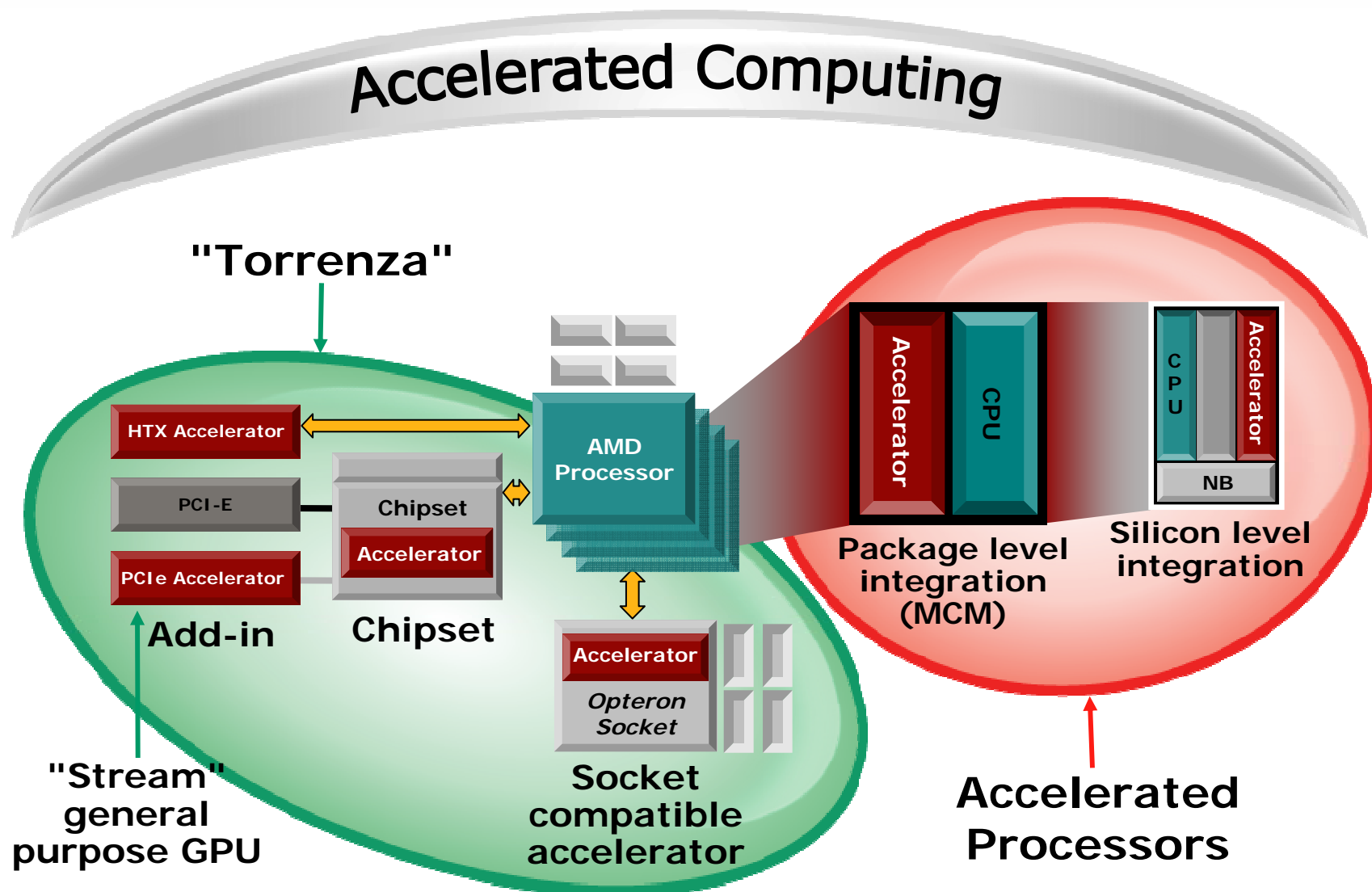
**x86 applications, workloads and usage models
continue to rapidly diversify**

The Accelerated Processing Imperative



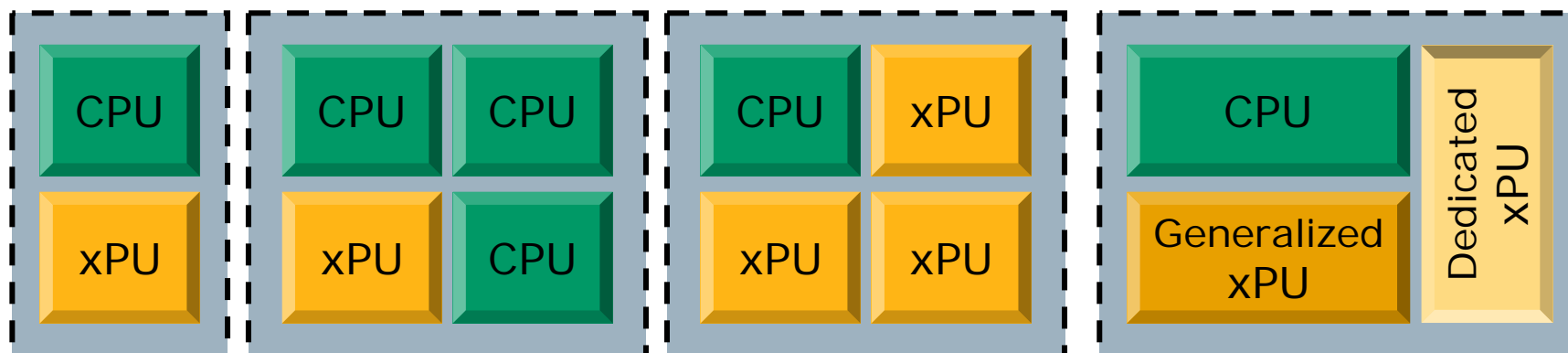
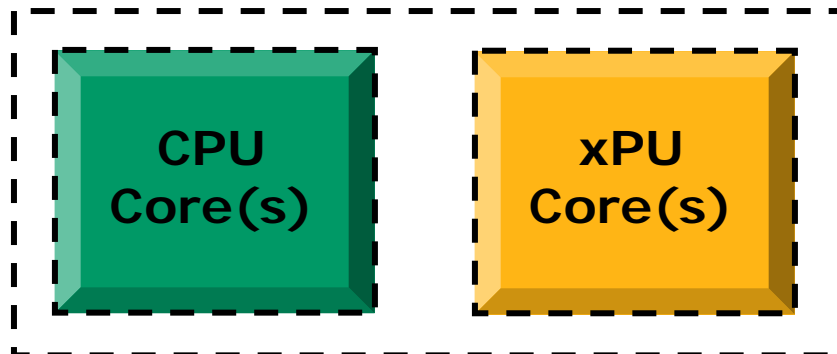
By the end of the decade, homogenous multi-core becomes increasingly inadequate

Continuum of Solutions



A New Product Category for a New Era

Accelerated Processing Units

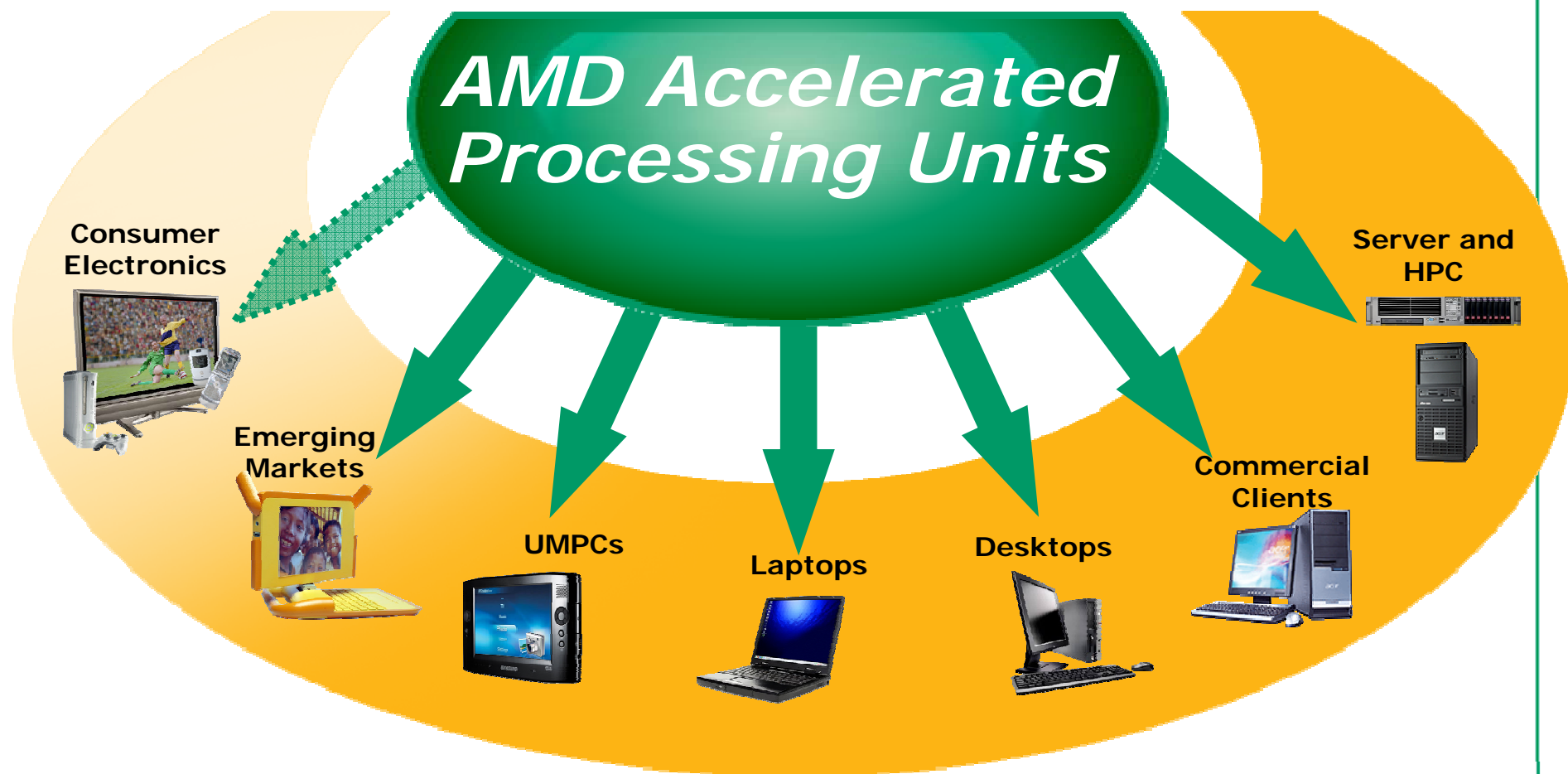


New category defined by a combination of CPU and non-CPU cores integrated on silicon

AMD x86-based Accelerated Processors Spanning Palmtops to PetaFLOPS



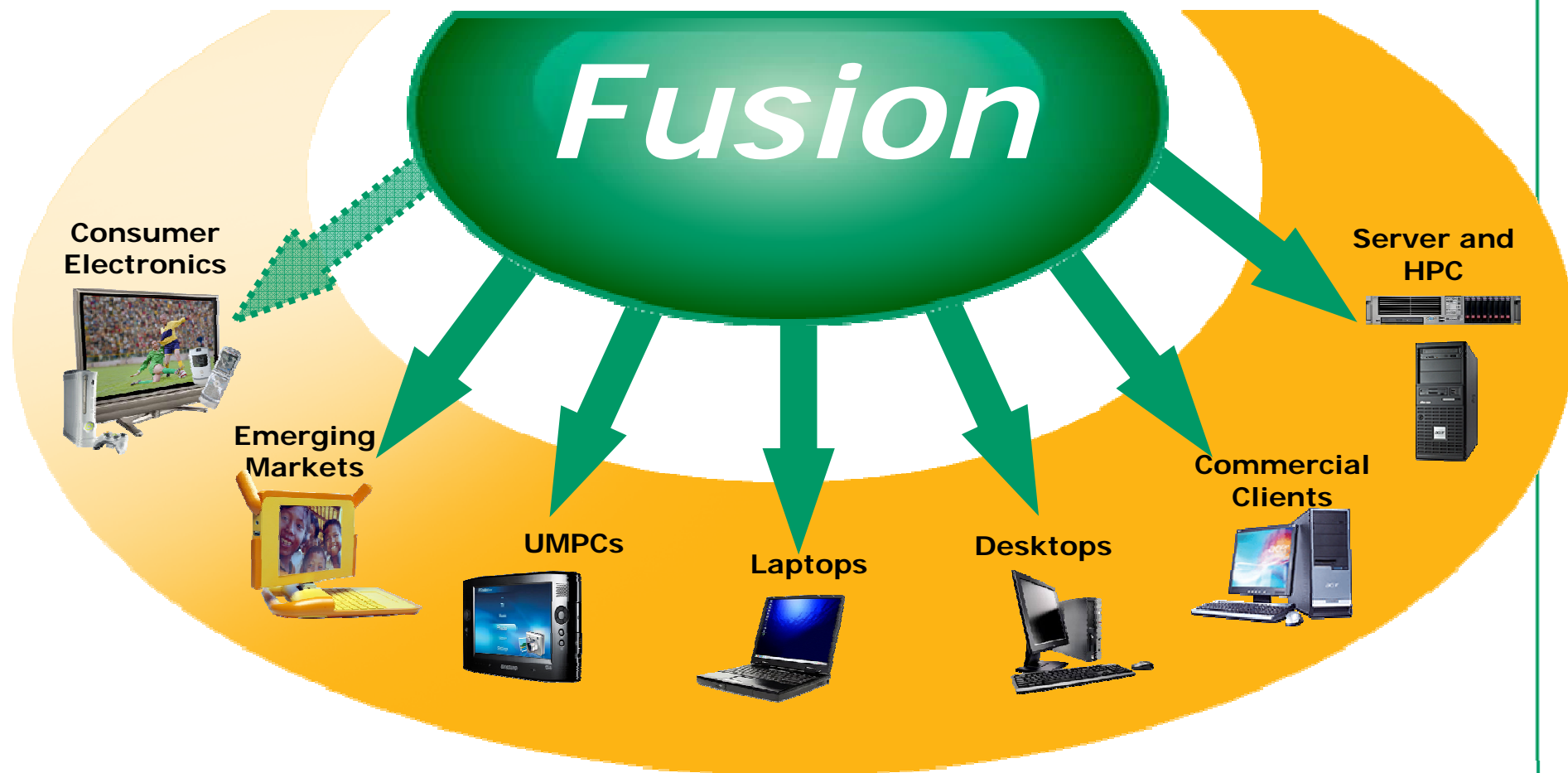
Full x86 software compatibility across all form factors



AMD x86-based Accelerated Processors Spanning Palmtops to PetaFLOPS



Full x86 software compatibility across all form factors



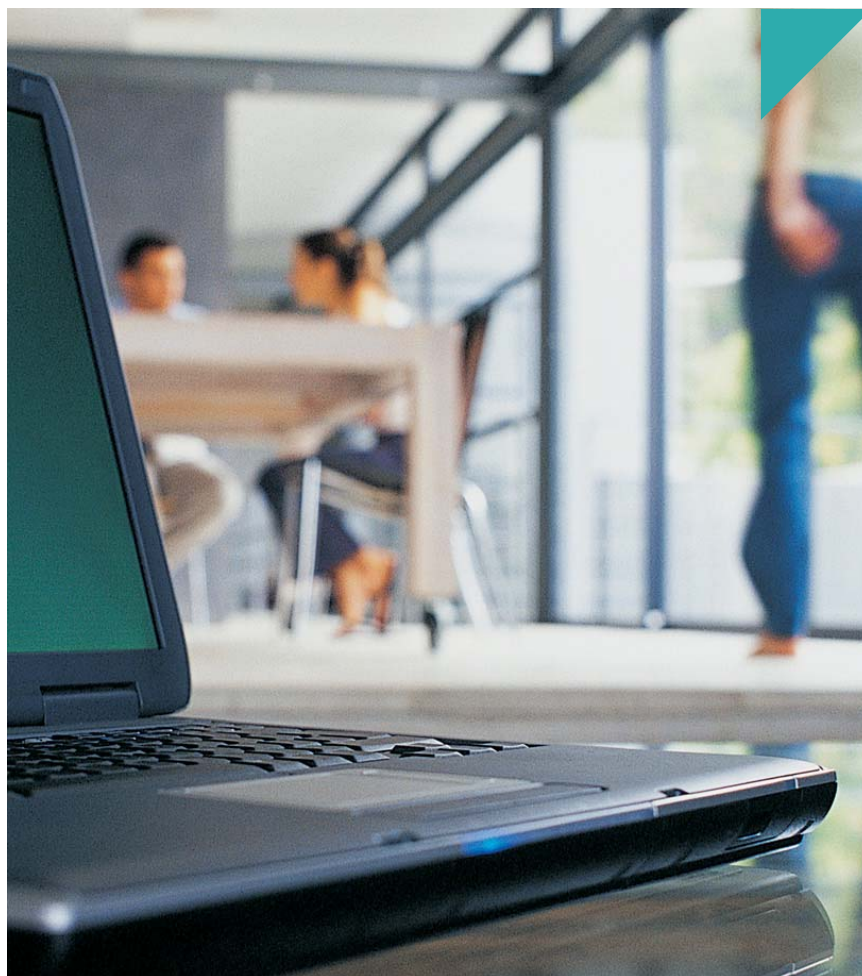
For Consumers: The Coming Era of Ubiquitous 3D Graphics

The launch of Windows Vista™ will rapidly accelerate the already growing role of quality 3D graphics in the mainstream client computing experience



Soon, achieving the optimum experience will *require* a robust level of 3D graphics

For Consumers: The Increasing Need for “No Sacrifice” Mobility



Goals

All day battery life

HD video/audio

3D gaming performance

Seamless connectivity

Rock solid stability

Lightweight

Cool, quiet

Cost-effective

First AMD Fusion Product: Accelerated Processor Combining CPU and GPU



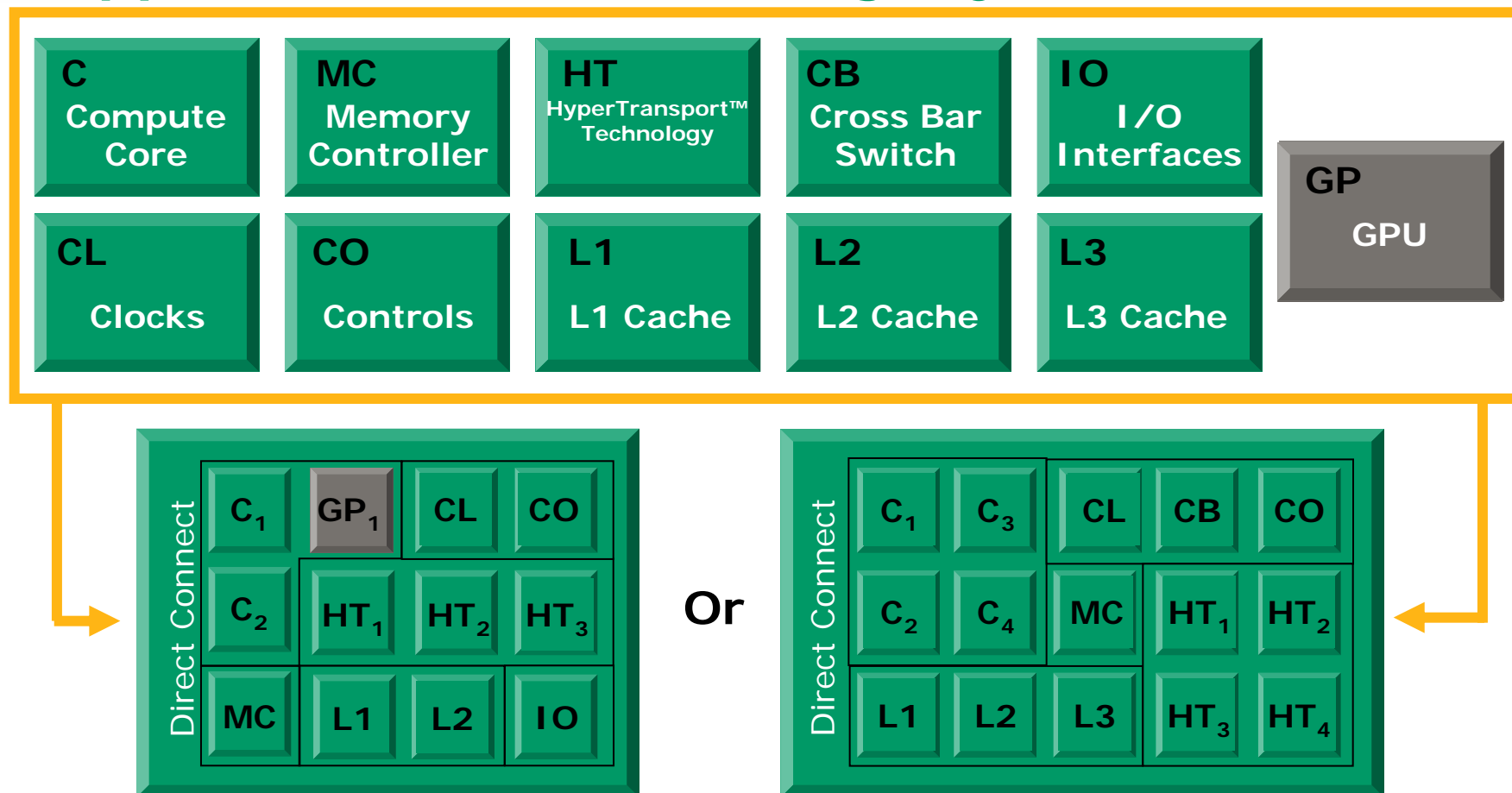
FUSION VISION

Create the optimal computing experience for an increasingly mobile, graphics- and media-centric world

Deliver step-function improvements in microprocessor performance-per-watt-per-dollar over today's CPU-only architectures

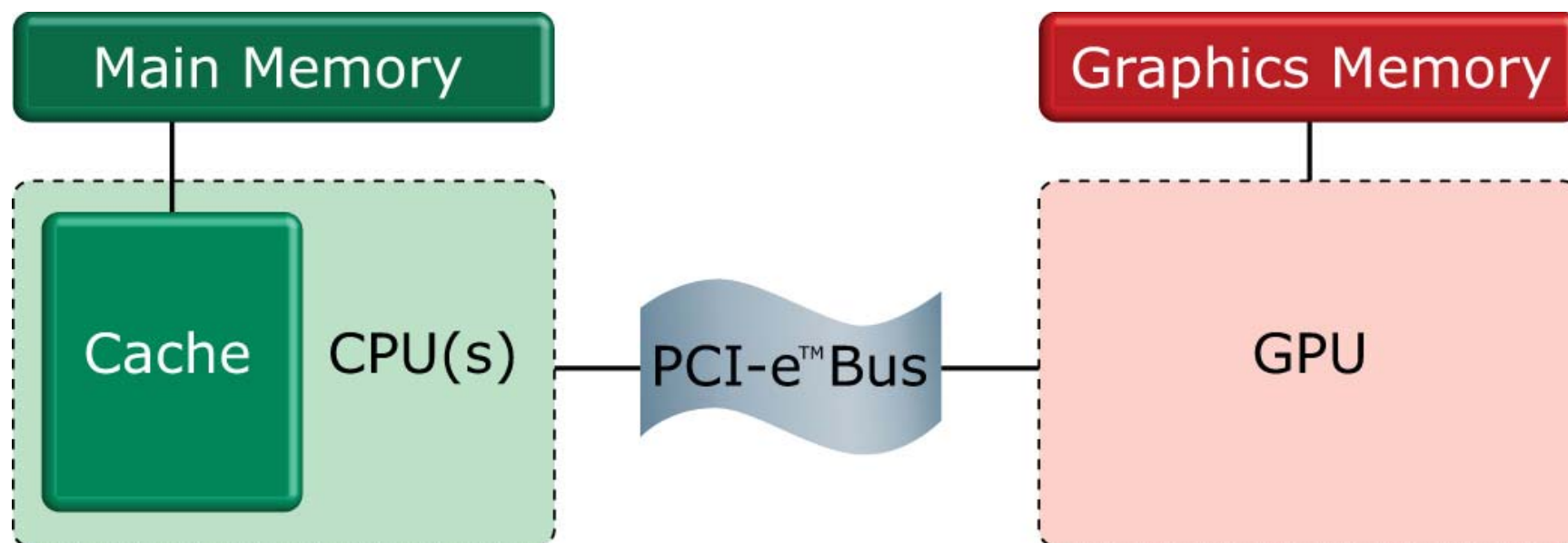
Continue to scale x86 from palmtops to petaFLOPS by enabling new x86 computing paradigms, classes and form factors

AMD's Flexible, Modular Design Approach for Increased Agility



Modular approach can accelerate time to market, increase flexibility and agility

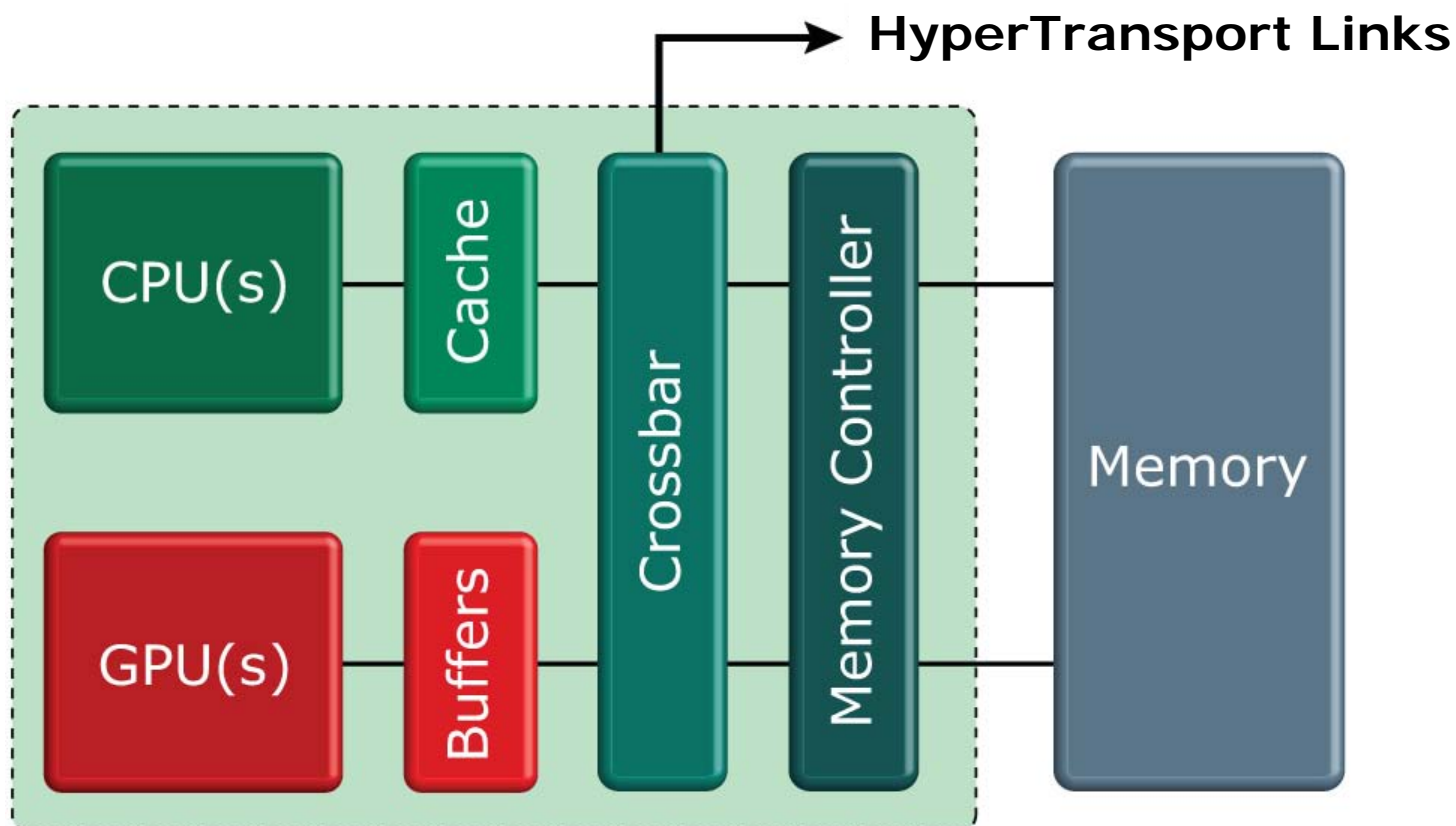
The Efficiency Benefits of Silicon-Level Integration



Non-optimized partitioning

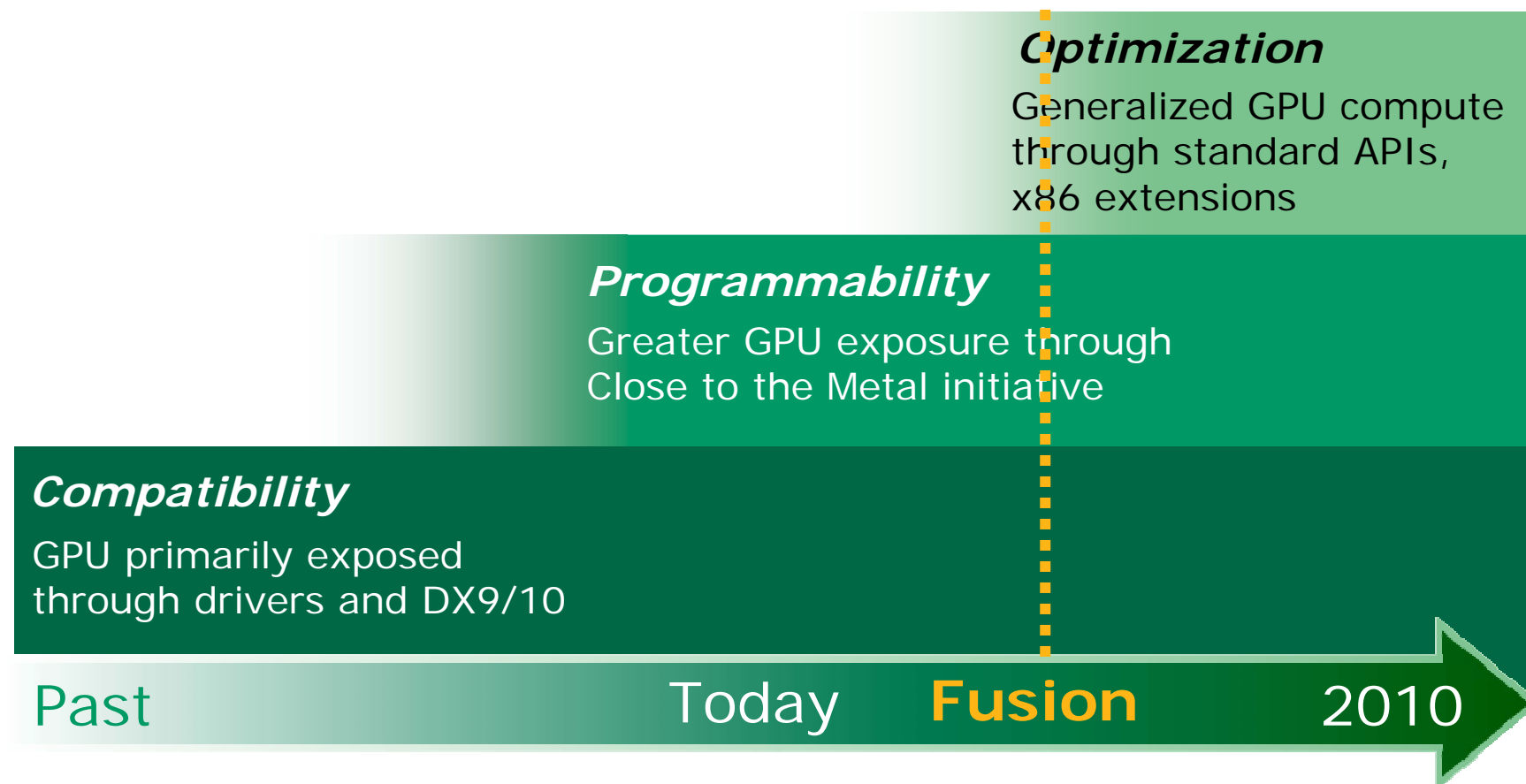
- Power
- Compute capability
- Cost
- Size

The Efficiency Benefits of Silicon-Level Integration



Planned step-function improvement in power/performance with cost reduction opportunities

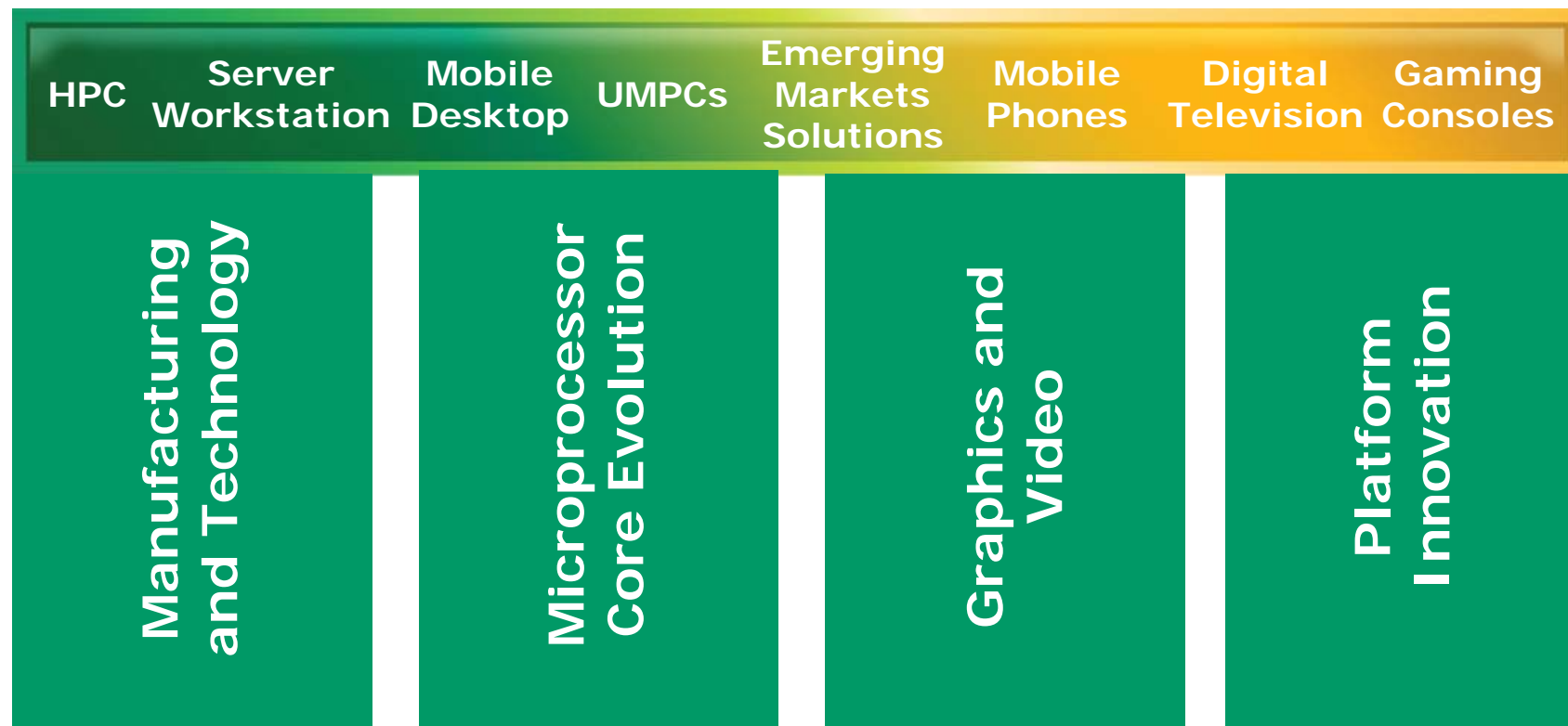
Non-Disruptive Software Transition



First Fusion processors: Immediate benefits using existing programming model

Unleashing the Processing Powerhouse

Bridging traditional computing and consumer electronics



An aligned vision for the PC and CE markets

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